

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10082599	FILING DATE 02/22/2002	CLASS 438	SUBCLASS 345.31	GAU 1703 2842	EXAMINER <i>Mills Moore</i>
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\*\*APPLICANTS: Carpenter Craig; Dando Ross; Mardian Allen; Hamer Kevin; Cantin Raynald; Campbell Philip; Tschepen Kimberly; Mercil Randy;

\*\*CONTINUING DATA VERIFIED:

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\*\*FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO MI22-1941
Verified and Acknowledged Examiner's initials			
TITLE : Interfacial structure for semiconductor substrate processing chambers and substrate transfer chambers and for semiconductor substrate processing chambers and accessory attachments,			
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for IOLG
Assistant Examiner		DRAWING	
		Sheets Draw.	Figs. Draw.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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DISCLAIMER			

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